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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ward D. Parkinson Et Al.

§ Group Art Unit:

Serial No.: 10/634,153

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Filed: August 4, 2003

For: Analog Phase Change
Memory

§ Examiner:

§ Atty. Dkt. No.: ITO.0553WO
(P16341)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

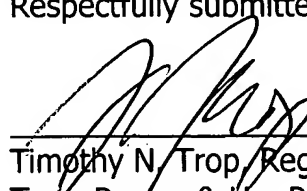
Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of which are enclosed. A copy of a communication dated October 11, 2004 from a foreign patent office in a counterpart application is also enclosed. I, the undersigned, hereby certify that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement. Please apply any charges or credits to Deposit Account No. 20-1504 (ITO.0553US).

Respectfully submitted,

October 19, 2004

Date

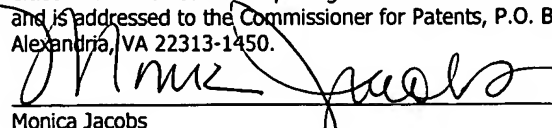

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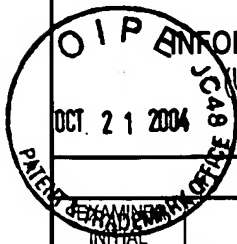
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Monica Jacobs



ATTY DOCKET NO. ITO.0553US (P16341)		SERIAL NO. 10/634,153
APPLICANT(S): Ward D. Parkinson et al.		
FILING DATE: August 4, 2003	GROUP ART UNIT:	

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A.	5,815,425	09/29/1998	Wong et al.			
	B.	6,044,004	03/28/2000	Kramer			
	C.						
	D.						
	E.						

U.S. PATENT APPLICATION PUBLICATIONS

	F.						
	G.						
	H.						
	I.						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	J.	EP 0 788 257	08/06/2997	Europe				
	K.	WO 93/04506	03/04/1993	PCT				
	L.	WO 95/20224	07/27/1995	PCT				
	M.	WO 03/058632	07/17/2003	PCT				
	N.							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	O.	A.F. Murray et al., <i>A User's Guide to Non-Volatile On-Chip Analogue Memory</i> , Electronics and Communication Engineering Journal, Institute of Electrical Engineers, London, GB, Vol. 10, No. 2, April 1, 1998, pgs. 53-63.
	P.	T.F. Blake, <i>Investigation of Ge₂Te₂Sb₅ Chalcogenide Films For Use As An Analog Memory</i> , Thesis, USA, March 2000, pgs. 1-107.
	Q.	
	R.	
	S.	
	T.	
	U.	

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.